Low Power Single 2-input AND Gate

CJ74AUP1G08 Logic

1 Introduction

The CJ74AUP1G08 provides the single 2-input AND function.

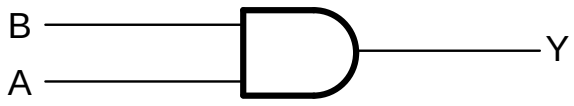
This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8V to 3.6V.

This device is fully specified for partial Power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2 Available Packages

PART NUMBER	PACKAGE
CJ74AUP1G08	SOT-23-5L
	SOT-353

Note: For all available packages, please refer to the part Orderable Information.



Simplified schematic

3 Features

- Wide supply voltage range from 0.8V to 3.6V
- Low static power consumption; I_{CC}=1uA (maximum)
- Inputs accept voltages up to 3.6V
- I_{OFF} circuitry provides partial Power-down mode operation
- Specified from: -40°C to +125°C

4 Applications

- ATCA Solutions
- Active Noise Cancellation (ANC)
- Barcode Scanner
- Blood Pressure Monitor
- CPAP Machine
- Cable Solutions
- DLP 3D Machine Vision, Hyperspectral Imaging, Optical Networking, and Spectroscopy
- E-Book
- Embedded PC
- Field Transmitter: Temperature or Pressure Sensor
- Fingerprint Biometrics
- HVAC: Heating, Ventilating, and Air Conditioning
- Network-Attached Storage (NAS)
- Server Motherboard and PSU
- Software Defined Radio (SDR)
- TV: High-Definition (HDTV), LCD, and Digital
- Video Communications System
- Wireless Data Access Card, Headset, Keyboard, Mouse, and LAN Card
- X-ray: Baggage Scanner, Medical, and Dental

5 Orderable Information

DEVICE	PACKAGE	OP TEMP	ECO PLAN	MSL	PACKING OPTION	SORT
CJ74AUP1G08M5N	SOT-23-5L	-40~125°C	RoHS & Green	Level 3 168HR	Tape and Reel 3000 Units / Reel	Active
CJ74AUP1G08R5N	SOT-353	-40~125°C	RoHS & Green	Level 3 168HR	Tape and Reel 3000 Units / Reel	Active

Note:

ECO PLAN: For the RoHS and Green certification standards of this product, please refer to the official report provided by JSCJ.

MSL: Moisture Sensitivity Level. Determined according to JEDEC industry standard classification.

SORT: Specifically defined as follows:
Active: Recommended for new products;
Customized: Products manufactured to meet the specific needs of customers;
Preview: The device has been released and has not been fully mass produced. The sample may or may not be available;
NoRD: It is not recommended to use the device for new design. The device is only produced for the needs of existing customers;
Obsolete: The device has been discontinued.

6 Pin Configuration and Marking Information

6.1 Pin Configuration

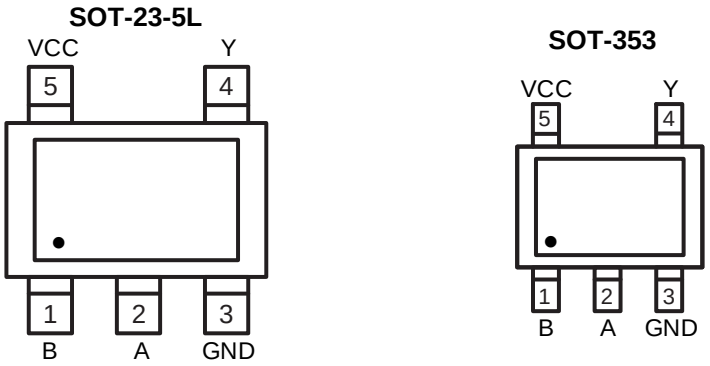


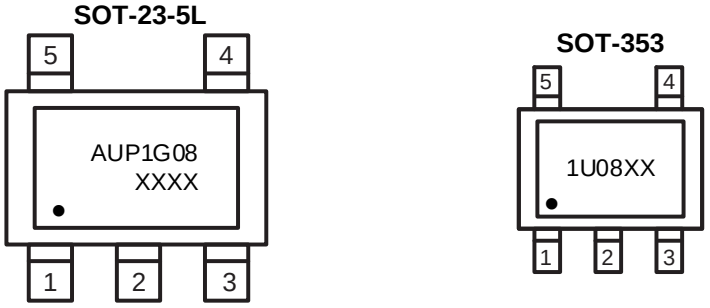
Figure 6-1 Pin configuration

6.2 Pin Function

PIN		I/O(1)	DESCRIPTION
No.	NAME		
1	B	I	Data input
2	A	I	Data input
3	GND	G	Ground (0V)
4	Y	O	Data output
5	VCC	P	Supply voltage

(1) I-Input, O-Output, P-Power, G-Ground

6.3 Marking Information



XXXX or XX: Code, indicates weekly record information.

7 Specifications

7.1 Absolute Maximum Ratings

T_{amb}=25°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CC}	Supply voltage	-	-0.5	+4.6	V
I_{IK}	Input clamping current	V_I < 0V	-50	-	mA
V_I	Input voltage	-(1)	-0.5	+4.6	V
I_{OK}	Output clamping current	V_O < 0V	-	±20	mA
V_O	Output voltage	Active mode and power-down mode(1)	-0.5	+4.6	V
I_O	Output current	V_O=0V to V_{CC}	-	±20	mA
I_{CC}	Supply current	-	-	+50	mA
I_{GND}	Ground current	-	-50	-	mA
T_{stg}	Storage temperature	-	-65	+150	°C
P_{tot}	Total power dissipation	-	-	250	mW
T_L	Soldering temperature	-	-	260	°C

(1) The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

7.2 Recommended Operating Conditions

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CC}	Supply voltage	-	0.8	-	3.6	V
V_I	Input voltage	-	0	-	3.6	V
V_O	Output voltage	Active mode	0	-	V_{CC}	V
		Power-down mode; V_{CC}=0V	0	-	3.6	V
T_{amb}	Ambient temperature	-	-40	-	+125	°C
Δt/ΔV	Input transition rise and fall rate	V_{CC}=0.8V to 3.6V	0	-	200	ns/V

7.3 Electrical Characteristics

7.3.1 DC Characteristics 1

T_{amb}=25°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{IH}	HIGH-level input voltage	V_{CC}=0.8V		0.70xV_{CC}	-	-	V
		V_{CC}=0.9V to 1.95V		0.65xV_{CC}	-	-	V
		V_{CC}=2.3V to 2.7V		1.6	-	-	V
		V_{CC}=3.0V to 3.6V		2.0	-	-	V
V_{IL}	LOW-level input voltage	V_{CC}=0.8V		-	-	0.30xV_{CC}	V
		V_{CC}=0.9V to 1.95V		-	-	0.35xV_{CC}	V
		V_{CC}=2.3V to 2.7V		-	-	0.7	V
		V_{CC}=3.0V to 3.6V		-	-	0.9	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}	I_O=-20uA; V_{CC}=0.8V to 3.6V	V_{CC}-0.1	-	-	V
			I_O=-1.1mA; V_{CC}=1.1V	0.75xV_{CC}	-	-	V
			I_O=-1.7mA; V_{CC}=1.4V	1.11	-	-	V
			I_O=-1.9mA; V_{CC}=1.65V	1.32	-	-	V
			I_O=-2.3mA; V_{CC}=2.3V	2.05	-	-	V
			I_O=-3.1mA; V_{CC}=2.3V	1.9	-	-	V
			I_O=-2.7mA; V_{CC}=3.0V	2.72	-	-	V
			I_O=-4.0mA; V_{CC}=3.0V	2.6	-	-	V
V_{OL}	LOW-level output voltage	V_I = V_{IH} or V_{IL}	I_O=20uA; V_{CC}=0.8V to 3.6V	-	-	0.1	V
			I_O=1.1mA; V_{CC}=1.1V	-	-	0.3xV_{CC}	V
			I_O=1.7mA; V_{CC}=1.4V	-	-	0.31	V
			I_O=1.9mA; V_{CC}=1.65V	-	-	0.31	V
			I_O=2.3mA; V_{CC}=2.3V	-	-	0.31	V
			I_O=3.1mA; V_{CC}=2.3V	-	-	0.44	V
			I_O=2.7mA; V_{CC}=3.0V	-	-	0.31	V
			I_O=4.0mA; V_{CC}=3.0V	-	-	0.44	V
I_I	Input leakage current	V_I=GND to 3.6V; V_{CC}=0V to 3.6V		-	-	±1.0	uA
I_{OFF}	Power-off leakage current	V_I or V_O=0V to 3.6V; V_{CC}=0V		-	-	±1.0	uA
ΔI_{OFF}	Additional power-off leakage current	V_I or V_O=0V to 3.6V; V_{CC}=0V to 0.2V		-	-	±1.0	uA
I_{CC}	Supply current	V_I=GND or V_{CC}; I_O=0A; V_{CC}=0.8V to 3.6V		-	-	1.0	uA
ΔI_{CC}	Additional supply current	V_I=V_{CC}-0.6V; I_O=0A; V_{CC}=3.3V(1)		-	-	40	uA
C_I	Input capacitance	V_{CC}=0V to 3.6V; V_I=GND or V_{CC}		-	0.8	-	pF
C_O	Output capacitance	V_O=GND; V_{CC}=0V		-	1.7	-	pF

(1) One input at V_{CC}-0.6V, other input at V_{CC} or GND.

7.3.2 DC Characteristics 2

T_{amb}=-40°C to +85°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{IH}	HIGH-level input voltage	V_{CC}=0.8V		0.70xV_{CC}	-	-	V
		V_{CC}=0.9V to 1.95V		0.65xV_{CC}	-	-	V
		V_{CC}=2.3V to 2.7V		1.6	-	-	V
		V_{CC}=3.0V to 3.6V		2.0	-	-	V
V_{IL}	LOW-level input voltage	V_{CC}=0.8V		-	-	0.30xV_{CC}	V
		V_{CC}=0.9V to 1.95V		-	-	0.35xV_{CC}	V
		V_{CC}=2.3V to 2.7V		-	-	0.7	V
		V_{CC}=3.0V to 3.6V		-	-	0.9	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}	I_O=-20uA; V_{CC}=0.8V to 3.6V	V_{CC}-0.1	-	-	V
			I_O=-1.1mA; V_{CC}=1.1V	0.7xV_{CC}	-	-	V
			I_O=-1.7mA; V_{CC}=1.4V	1.03	-	-	V
			I_O=-1.9mA; V_{CC}=1.65V	1.30	-	-	V
			I_O=-2.3mA; V_{CC}=2.3V	1.97	-	-	V
			I_O=-3.1mA; V_{CC}=2.3V	1.85	-	-	V
			I_O=-2.7mA; V_{CC}=3.0V	2.67	-	-	V
			I_O=-4.0mA; V_{CC}=3.0V	2.55	-	-	V
V_{OL}	LOW-level output voltage	V_I = V_{IH} or V_{IL}	I_O=20uA; V_{CC}=0.8V to 3.6V	-	-	0.1	V
			I_O=1.1mA; V_{CC}=1.1V	-	-	0.3xV_{CC}	V
			I_O=1.7mA; V_{CC}=1.4V	-	-	0.37	V
			I_O=1.9mA; V_{CC}=1.65V	-	-	0.35	V
			I_O=2.3mA; V_{CC}=2.3V	-	-	0.33	V
			I_O=3.1mA; V_{CC}=2.3V	-	-	0.45	V
			I_O=2.7mA; V_{CC}=3.0V	-	-	0.33	V
			I_O=4.0mA; V_{CC}=3.0V	-	-	0.45	V
I_I	Input leakage current	V_I=GND to 3.6V; V_{CC}=0V to 3.6V		-	-	±1.0	uA
I_{OFF}	Power-off leakage current	V_I or V_O=0V to 3.6V; V_{CC}=0V		-	-	±1.0	uA
ΔI_{OFF}	Additional power-off leakage current	V_I or V_O=0V to 3.6V; V_{CC}=0V to 0.2V		-	-	±1.0	uA
I_{CC}	Supply current	V_I=GND or V_{CC}; I_O=0A; V_{CC}=0.8V to 3.6V		-	-	1.0	uA
ΔI_{CC}	Additional supply current	V_I=V_{CC}-0.6V; I_O=0A; V_{CC}=3.3V(1)		-	-	50	uA

(1) One input at V_{CC}-0.6V, other input at V_{CC} or GND.

7.3.3 DC Characteristics 3

T_{amb}=-40°C to +125°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{IH}	HIGH-level input voltage	V_{CC}=0.8V		0.75xV_{CC}	-	-	V
		V_{CC}=0.9V to 1.95V		0.70xV_{CC}	-	-	V
		V_{CC}=2.3V to 2.7V		1.6	-	-	V
		V_{CC}=3.0V to 3.6V		2.0	-	-	V
V_{IL}	LOW-level input voltage	V_{CC}=0.8V		-	-	0.25xV_{CC}	V
		V_{CC}=0.9V to 1.95V		-	-	0.30xV_{CC}	V
		V_{CC}=2.3V to 2.7V		-	-	0.7	V
		V_{CC}=3.0V to 3.6V		-	-	0.9	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}	I_O=-20uA; V_{CC}=0.8V to 3.6V	V_{CC}-0.11	-	-	V
			I_O=-1.1mA; V_{CC}=1.1V	0.6xV_{CC}	-	-	V
			I_O=-1.7mA; V_{CC}=1.4V	0.93	-	-	V
			I_O=-1.9mA; V_{CC}=1.65V	1.17	-	-	V
			I_O=-2.3mA; V_{CC}=2.3V	1.77	-	-	V
			I_O=-3.1mA; V_{CC}=2.3V	1.67	-	-	V
			I_O=-2.7mA; V_{CC}=3.0V	2.40	-	-	V
			I_O=-4.0mA; V_{CC}=3.0V	2.30	-	-	V
V_{OL}	LOW-level output voltage	V_I = V_{IH} or V_{IL}	I_O=20uA; V_{CC}=0.8V to 3.6V	-	-	0.11	V
			I_O=1.1mA; V_{CC}=1.1V	-	-	0.33xV_{CC}	V
			I_O=1.7mA; V_{CC}=1.4V	-	-	0.41	V
			I_O=1.9mA; V_{CC}=1.65V	-	-	0.39	V
			I_O=2.3mA; V_{CC}=2.3V	-	-	0.36	V
			I_O=3.1mA; V_{CC}=2.3V	-	-	0.50	V
			I_O=2.7mA; V_{CC}=3.0V	-	-	0.36	V
			I_O=4.0mA; V_{CC}=3.0V	-	-	0.50	V
I_I	Input leakage current	V_I=GND to 3.6V; V_{CC}=0V to 3.6V		-	-	±1.0	uA
I_{OFF}	Power-off leakage current	V_I or V_O=0V to 3.6V; V_{CC}=0V		-	-	±1.0	uA
ΔI_{OFF}	Additional power-off leakage current	V_I or V_O=0V to 3.6V; V_{CC}=0V to 0.2V		-	-	±1.0	uA
I_{CC}	Supply current	V_I=GND or V_{CC}; I_O=0A; V_{CC}=0.8V to 3.6V		-	-	1.4	uA
ΔI_{CC}	Additional supply current	V_I=V_{CC}-0.6V; I_O=0A; V_{CC}=3.3V(1)		-	-	75	uA

(1) One input at V_{CC}-0.6V, other input at V_{CC} or GND.

7.3.4 AC Characteristics 1

T_{amb}=25°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP. (1)	MAX.	UNIT
t_{pd}	Propagation delay	A, B to Y; C_L=5pF See Figure 8-5(2)	V_{CC}=0.8V	-	17.0	-	ns
			V_{CC}=1.1V to 1.3V	2.6	5.1	10.8	ns
			V_{CC}=1.4V to 1.6V	1.6	3.7	6.5	ns
			V_{CC}=1.65V to 1.95V	1.3	3.0	5.2	ns
			V_{CC}=2.3V to 2.7V	1.1	2.4	4.0	ns
			V_{CC}=3.0V to 3.6V	1.0	2.2	3.5	ns
		A, B to Y; C_L=10pF See Figure 8-5(2)	V_{CC}=0.8V	-	20.6	-	ns
			V_{CC}=1.1V to 1.3V	2.4	6.0	12.5	ns
			V_{CC}=1.4V to 1.6V	2.0	4.3	7.6	ns
			V_{CC}=1.65V to 1.95V	1.7	3.6	6.1	ns
			V_{CC}=2.3V to 2.7V	1.4	2.9	4.8	ns
			V_{CC}=3.0V to 3.6V	1.3	2.7	4.2	ns
		A, B to Y; C_L=15pF See Figure 8-5(2)	V_{CC}=0.8V	-	24.1	-	ns
			V_{CC}=1.1V to 1.3V	3.4	6.8	14.2	ns
			V_{CC}=1.4V to 1.6V	2.3	4.9	8.6	ns
			V_{CC}=1.65V to 1.95V	1.9	4.0	6.9	ns
			V_{CC}=2.3V to 2.7V	1.7	3.4	5.5	ns
			V_{CC}=3.0V to 3.6V	1.5	3.1	4.8	ns
		A, B to Y; C_L=30pF See Figure 8-5(2)	V_{CC}=0.8V	-	34.4	-	ns
			V_{CC}=1.1V to 1.3V	4.6	9.1	19.4	ns
			V_{CC}=1.4V to 1.6V	3.4	6.4	11.5	ns
			V_{CC}=1.65V to 1.95V	2.6	5.3	9.1	ns
			V_{CC}=2.3V to 2.7V	2.3	4.5	7.2	ns
			V_{CC}=3.0V to 3.6V	2.2	4.2	6.2	ns
C_{PD}	Power dissipation capacitance	f=1MHz; V_I=GND to V_{CC}(3)	V_{CC}=0.8V	-	2.5	-	pF
			V_{CC}=1.1V to 1.3V	-	2.7	-	pF
			V_{CC}=1.4V to 1.6V	-	2.8	-	pF
			V_{CC}=1.65V to 1.95V	-	2.9	-	pF
			V_{CC}=2.3V to 2.7V	-	3.5	-	pF
			V_{CC}=3.0V to 3.6V	-	4.0	-	pF

(1) All typical values are measured at nominal V_{CC}.

(2) t_{pd} is the same as t_{PLH} and t_{PHL}.

(3) C_{PD} is used to determine the dynamic power dissipation (P_D in uW).
$P_D=C_{PD}xV_{CC}^2xf_ixN+\Sigma(C_LxV_{CC}^2xf_o)$ where:
f_i=input frequency in MHz; f_o=output frequency in MHz;
C_L=output load capacitance in pF; V_{CC}=supply voltage in V;
N=number of inputs switching; $\Sigma(C_LxV_{CC}^2xf_o)$=sum of the outputs.

7.3.5 AC Characteristics 2

T_{amb}=-40°C to +85°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	MAX.	MAX.	UNIT
t_{pd}	Propagation delay	A, B to Y; C_L=5pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	2.1	-	11.7	ns
			V_{CC}=1.4V to 1.6V	1.5	-	7.5	ns
			V_{CC}=1.65V to 1.95V	1.3	-	6.1	ns
			V_{CC}=2.3V to 2.7V	1.0	-	4.8	ns
			V_{CC}=3.0V to 3.6V	0.9	-	4.3	ns
		A, B to Y; C_L=10pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	2.2	-	13.6	ns
			V_{CC}=1.4V to 1.6V	1.8	-	8.9	ns
			V_{CC}=1.65V to 1.95V	1.6	-	7.2	ns
			V_{CC}=2.3V to 2.7V	1.3	-	5.7	ns
			V_{CC}=3.0V to 3.6V	1.2	-	4.7	ns
		A, B to Y; C_L=15pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	3.1	-	15.7	ns
			V_{CC}=1.4V to 1.6V	2.1	-	10.1	ns
			V_{CC}=1.65V to 1.95V	1.8	-	8.2	ns
			V_{CC}=2.3V to 2.7V	1.6	-	6.5	ns
			V_{CC}=3.0V to 3.6V	1.5	-	5.9	ns
		A, B to Y; C_L=30pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	4.1	-	21.8	ns
			V_{CC}=1.4V to 1.6V	2.9	-	13.6	ns
			V_{CC}=1.65V to 1.95V	2.4	-	10.9	ns
			V_{CC}=2.3V to 2.7V	2.2	-	8.6	ns
			V_{CC}=3.0V to 3.6V	2.1	-	7.5	ns

(1) t_{pd} is the same as t_{PLH} and t_{PHL}.

7.3.6 AC Characteristics 3

T_{amb}=-40°C to +125°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	MAX.	MAX.	UNIT
t_{pd}	Propagation delay	A, B to Y; C_L=5pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	2.1	-	12.9	ns
			V_{CC}=1.4V to 1.6V	1.5	-	8.3	ns
			V_{CC}=1.65V to 1.95V	1.3	-	6.7	ns
			V_{CC}=2.3V to 2.7V	1.0	-	5.3	ns
			V_{CC}=3.0V to 3.6V	0.9	-	4.8	ns
		A, B to Y; C_L=10pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	2.2	-	15.0	ns
			V_{CC}=1.4V to 1.6V	1.8	-	9.8	ns
			V_{CC}=1.65V to 1.95V	1.6	-	7.9	ns
			V_{CC}=2.3V to 2.7V	1.3	-	6.3	ns
			V_{CC}=3.0V to 3.6V	1.2	-	5.2	ns
		A, B to Y; C_L=15pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	3.1	-	17.3	ns
			V_{CC}=1.4V to 1.6V	2.1	-	11.2	ns
			V_{CC}=1.65V to 1.95V	1.8	-	9.0	ns
			V_{CC}=2.3V to 2.7V	1.6	-	7.2	ns
			V_{CC}=3.0V to 3.6V	1.5	-	6.5	ns
		A, B to Y; C_L=30pF See Figure 8-5(1)	V_{CC}=1.1V to 1.3V	4.1	-	24.0	ns
			V_{CC}=1.4V to 1.6V	2.9	-	15.0	ns
			V_{CC}=1.65V to 1.95V	2.4	-	12.1	ns
			V_{CC}=2.3V to 2.7V	2.2	-	9.5	ns
			V_{CC}=3.0V to 3.6V	2.1	-	8.3	ns

(1) t_{pd} is the same as t_{PLH} and t_{PHL}.

8 Detailed Description

8.1 Overview

The CJ74AUP1G08 provides the single 2-input AND function.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8V to 3.6V.

This device is fully specified for partial Power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

8.2 Functional Block Diagram

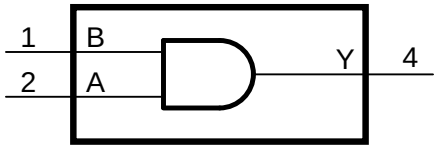


Figure 8-1 Logic symbol

Figure 8-2 IEC logic symbol

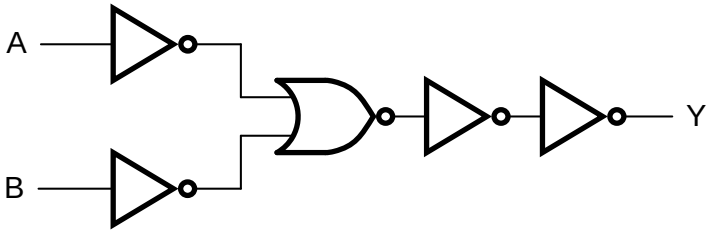


Figure 8-3 Logic diagram

8.3 Function Table

INPUT		OUTPUT
A	**B**	**Y**
L	L	L
L	H	L
H	L	L
H	H	H

Note: H=HIGH voltage level; L=LOW voltage level.

8.4 Testing Circuit

8.4.1 AC Testing Circuit

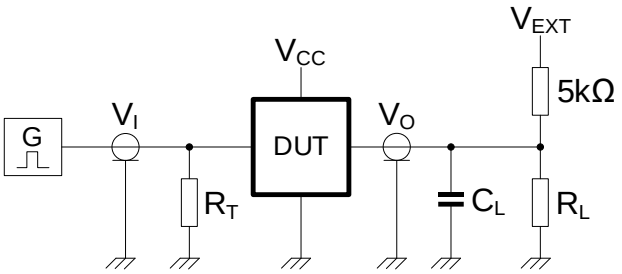


Figure 8-4 Test circuit for measuring switching times

Definitions for test circuit:

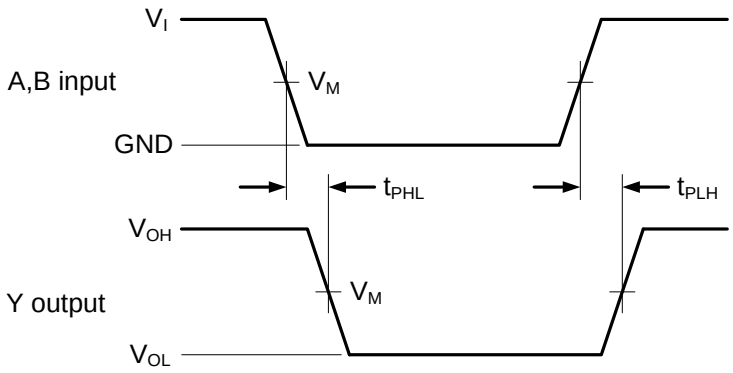
R_L=Load resistance.

C_L=Load capacitance including jig and probe capacitance.

R_T=Termination resistance should be equal to the output impedance Z_o of the pulse generator.

V_{EXT}=External voltage for measuring switching times.

8.4.2 AC Testing Waveforms



Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure 8-5 The data input (A or B) to output (Y) propagation delays

8.4.3 Measurement Points

SUPPLY VOLTAGE	OUTPUT	INPUT		
V_{CC}	V_M	V_M	V_I	$t_r=t_f$
0.8V to 3.6V	0.5xV_{CC}	0.5xV_{CC}	V_{CC}	≤3.0ns

8.4.4 Test Data

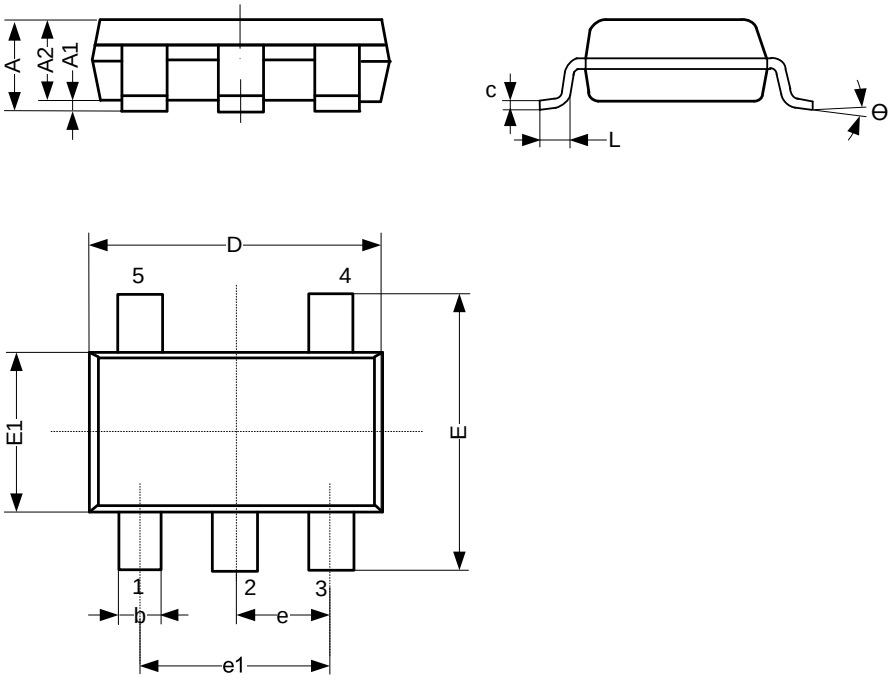
SUPPLY VOLTAGE	LOAD		V_{EXT}		
V_{CC}	C_L	R_L(1)	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
0.8V to 3.6V	5pF, 10pF, 15pF and 30pF	5kΩ or 1MΩ	Open	GND	2xV_{CC}

(1) For measuring enable and disable times R_L=5kΩ, for measuring propagation delays, setup and hold times and pulse width R_L=1MΩ.

9 Mechanical Information

9.1 SOT-23-5L Mechanical Information

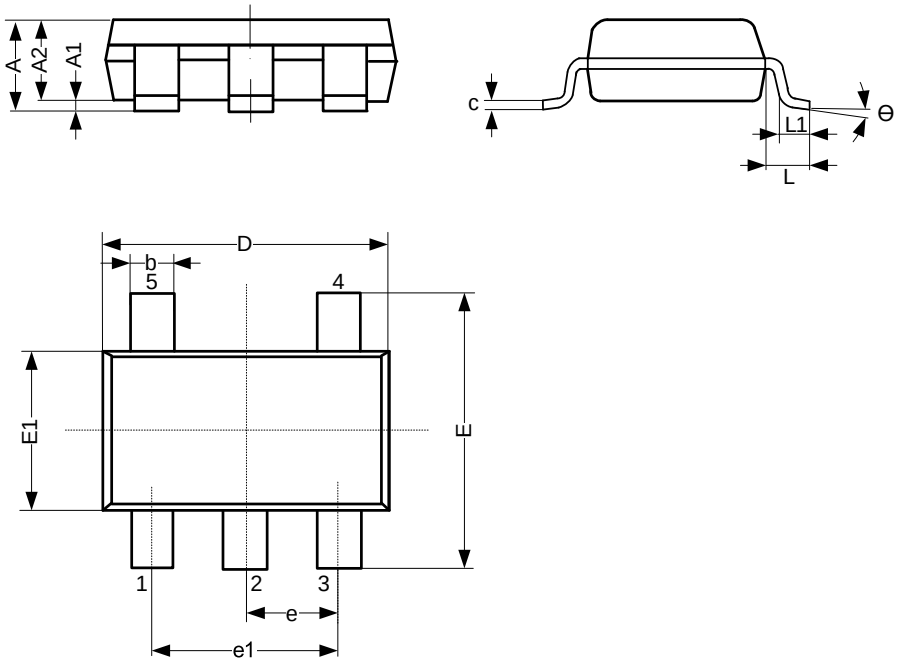
9.1.1 SOT-23-5L Outline Dimensions



SYMBOL	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	-	-	1.26
A1	0.00	-	0.12
A2	1.00	-	1.20
b	0.30	-	0.50
c	0.10	-	0.20
D	2.82	-	3.02
E	2.60	-	3.00
E1	1.50	-	1.70
e	0.95 BSC		
e1	1.80	-	2.00
L	0.30	-	0.60
Θ	0°	-	8°
Unit: mm			

9.2 SOT-353 Mechanical Information

9.2.1 SOT-353 Outline Dimensions



SYMBOL	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.90	-	1.10
A1	0.00	-	0.10
A2	0.90	-	1.00
b	0.15	-	0.35
c	0.11	-	0.175
D	2.00	-	2.20
E	2.15	-	2.45
E1	1.15	-	1.35
e	0.65 BSC		
e1	1.20	-	1.40
L	-	0.525	-
L1	0.26	-	0.46
θ	0°	-	8°
Unit: mm			

10 Notes and Revision History

10.1 Associated Product Family and Others

To view other products of the same type or IC products of other types, click the official website of JSCJ -- ***https:www.jscj-elec.com*** for more details.

10.2 Notes

Electrostatic Discharge Caution

This IC may be damaged by ESD. Relevant personnel shall comply with correct installation and use specifications to avoid ESD damage to the IC. If appropriate measures are not taken to prevent ESD damage, the hazards caused by ESD include but are not limited to degradation of integrated circuit performance or complete damage of integrated circuit. For some precision integrated circuits, a very small parameter change may cause the whole device to be inconsistent with its published specifications.

DISCLAIMER

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