Single Supply Translating Buffer/Line Driver; 3-state

CJ74LV1T126 Logic

1 Introduction

The CJ74LV1T126 is a single, level translating buffer/line driver with 3-state output.

The low threshold inputs support 1.8V input logic at V_{CC}=3.3V and can be used in 1.8V to 3.3V level up translation. In addition, the 5V tolerant input pins enable level down translation (3.3V to 2.5V output at V_{CC}=2.5V). The output level is referenced to the supply voltage and supports 1.8V, 2.5V, 3.3V and 5.0V CMOS levels.

2 Available Packages

PART NUMBER	PACKAGE
CJ74LV1T126	SOT-23-5L
	SOT-353

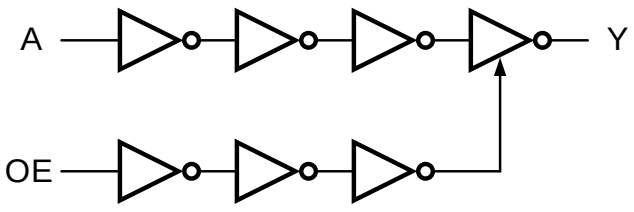
Note: For all available packages, please refer to the part Orderable Information.

3 Features

- Supply voltage:1.6V to 5.5V
- Up translation
  - 1.2V to 1.8V at V_{CC}=1.8V
  - 1.5V to 2.5V at V_{CC}=2.5V
  - 1.8V to 3.3V at V_{CC}=3.3V
  - 3.3V to 5.0V at V_{CC}=5.0V
- Down translation
  - 3.3V to 1.8V at V_{CC}=1.8V
  - 3.3V to 2.5V at V_{CC}=2.5V
  - 5.0V to 3.3V at V_{CC}=3.3V
- Inputs accept voltages to 5.5V
- Specified from -40°C to +125°C

4 Applications

- Telecom
- Portable applications
- Servers
- PC and notebooks



Logic diagram

5 Orderable Information

DEVICE	PACKAGE	OP TEMP	ECO PLAN	MSL	PACKING OPTION	SORT
CJ74LV1T126M5N	SOT-23-5L	-40~125°C	RoHS & Green	Level 3 168HR	Tape and Reel 3000 Units / Reel	Active
CJ74LV1T126R5N	SOT-353	-40~125°C	RoHS & Green	Level 3 168HR	Tape and Reel 3000 Units / Reel	Active

Note:

ECO PLAN: For the RoHS and Green certification standards of this product, please refer to the official report provided by JSCJ.

MSL: Moisture Sensitivity Level. Determined according to JEDEC industry standard classification.

SORT: Specifically defined as follows:
Active: Recommended for new products;
Customized: Products manufactured to meet the specific needs of customers;
Preview: The device has been released and has not been fully mass produced. The sample may or may not be available;
NoRD: It is not recommended to use the device for new design. The device is only produced for the needs of existing customers;
Obsolete: The device has been discontinued.

6 Pin Configuration and Marking Information

6.1 Pin Configuration

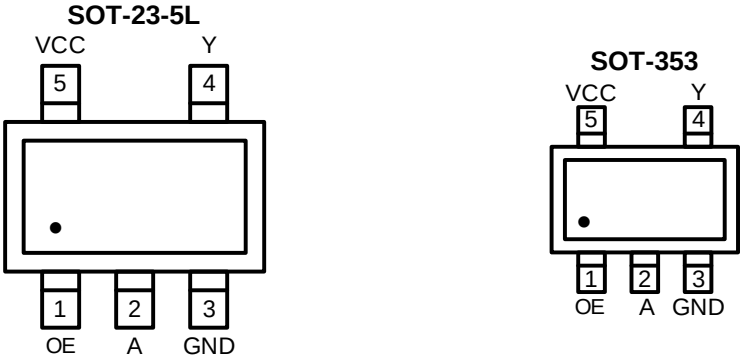


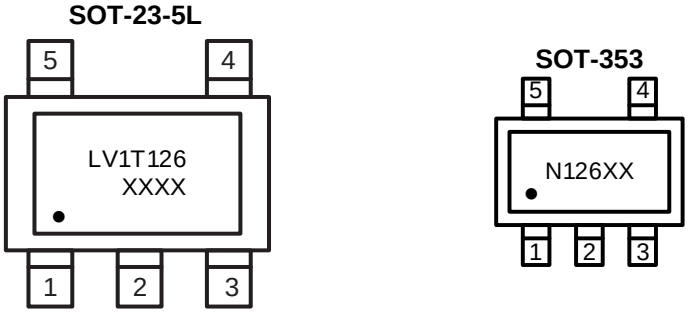
Figure 6-1 Pin configuration

6.2 Pin Function

PIN		I/O(1)	DESCRIPTION
No.	NAME		
1	OE	I	Output enable input
2	A	I	Data input
3	GND	G	Ground (0V)
4	Y	O	Data output
5	VCC	P	Supply voltage

(1) I-Input, O-Output, P-Power, G-Ground

6.3 Marking Information



XXXX or XX: Code, indicates weekly record information.

7 Specifications

7.1 Absolute Maximum Ratings

Voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CC}	Supply voltage	-	-0.5	+7.0	V
V_I	Input voltage	-	-0.5	+7.0	V
V_O	Output voltage	Output HIGH or LOW state	-0.5	V_{CC}+0.5	V
		Output in power-off state	-0.5	+4.6	V
I_{CC}	Supply current	-	-	50	mA
I_{GND}	Ground current	-	-50	-	mA
I_{IK}	Input clamping current	V_I < 0V	-20	-	mA
I_O	Output current	V_O=0V to V_{CC}	-	±25	mA
I_{OK}	Output clamping current	V_O<0V or V_O>V_{CC}	-	±20	mA
T_{stg}	Storage temperature	-	-65	+150	°C
T_L	Soldering temperature	10s	-	260	°C

Note: Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to GND. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

7.2 Recommended Operating Conditions

Voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CC}	Supply voltage	-	1.6	5.0	5.5	V
V_I	Input voltage	-	0	-	5.5	V
V_O	Output voltage	Output HIGH or LOW state	0	-	V_{CC}	V
T_{amb}	Ambient temperature	-	-40	-	+125	°C
Δt/ΔV	Input transition rise and fall rate	V_{CC}=1.8V to 5.0V	-	-	20	ns/V

7.3 ESD Ratings

SYMBOL	ESD RATINGS		VALUE	UNIT
$V_{ESD-HBM}$	Electrostatic discharge	Human body model (HBM)(1)	±2000	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

7.4 Electrical Characteristics

7.4.1 DC Characteristics 1

T_{amb}=25°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{IH}	HIGH-level input voltage	V_{CC}=1.65V to 1.8V		0.94	-	-	V
		V_{CC}=2.0V		0.99	-	-	V
		V_{CC}=2.25V to 2.5V		1.135	-	-	V
		V_{CC}=2.75V		1.21	-	-	V
		V_{CC}=3.0V to 3.3V		1.35	-	-	V
		V_{CC}=3.6V		1.47	-	-	V
		V_{CC}=4.5V to 5.0V		2.02	-	-	V
		V_{CC}=5.5V		2.10	-	-	V
V_{IL}	LOW-level input voltage	V_{CC}=1.65V to 2.0V		-	-	0.58	V
		V_{CC}=2.25V to 2.75V		-	-	0.75	V
		V_{CC}=3.0V to 3.6V		-	-	0.80	V
		V_{CC}=4.5V to 5.5V		-	-	0.80	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}	V_{CC}=1.65V to 5.5V; I_O=-20uA	V_{CC}-0.1	-	-	V
			V_{CC}=1.65V; I_O=-2mA	1.28	-	-	V
			V_{CC}=1.8V; I_O=-2mA	1.5	-	-	V
			V_{CC}=2.3V; I_O=-2.3mA	2.0	-	-	V
			V_{CC}=2.3V; I_O=-3mA	2.0	-	-	V
			V_{CC}=2.5V; I_O=-3mA	2.25	-	-	V
			V_{CC}=3.0V; I_O=-3mA	2.78	-	-	V
			V_{CC}=3.0V; I_O=-5.5mA	2.6	-	-	V
			V_{CC}=3.3V; I_O=-5.5mA	2.9	-	-	V
			V_{CC}=4.5V; I_O=-4mA	4.2	-	-	V
			V_{CC}=4.5V; I_O=-8mA	4.1	-	-	V
			V_{CC}=5.0V; I_O=-8mA	4.6	-	-	V
V_{OL}	LOW-level output voltage	V_I = V_{IH} or V_{IL}	V_{CC}=1.65V to 5.5V; I_O=20uA	-	-	0.1	V
			V_{CC}=1.65V; I_O=2mA	-	-	0.2	V
			V_{CC}=2.3V; I_O=2.3mA	-	-	0.1	V
			V_{CC}=2.3V; I_O=3mA	-	-	0.15	V
			V_{CC}=3.0V; I_O=3mA	-	-	0.1	V
			V_{CC}=3.0V; I_O=5.5mA	-	-	0.2	V
			V_{CC}=4.5V; I_O=4mA	-	-	0.15	V
			V_{CC}=4.5V; I_O=8mA	-	-	0.3	V

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_I	Input leakage current	V_I=V_{CC} or GND; V_{CC}=0V to 5.5V	-	-	±1	uA
I_{OZ}	OFF-state output current	V_{CC}=5V; V_I=V_{IH} or V_{IL}; V_O=V_{CC} or GND	-	-	±1	uA
I_{CC}	Supply current	V_I=V_{CC} or GND; I_O=0A; V_{CC}=1.8V, 2.5V, 3.3V, 5.0V	-	-	1	uA
ΔI_{CC}	Additional supply current	Per input pin; V_{CC}=1.8V; V_I=0.3V or 1.1V; I_O=0A; Other pins at V_{CC} or GND	-	-	10	uA
		Per input pin; V_{CC}=5.5V; V_I=0.3V or 3.4V; I_O=0A; Other pins at V_{CC} or GND	-	-	1.35	mA

7.4.2 DC Characteristics 2

T_{amb}=-40°C to +85°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{IH}	HIGH-level input voltage	V_{CC}=1.65V to 1.8V		1.0	-	-	V
		V_{CC}=2.0V		1.03	-	-	V
		V_{CC}=2.25V to 2.5V		1.18	-	-	V
		V_{CC}=2.75V		1.23	-	-	V
		V_{CC}=3.0V to 3.3V		1.37	-	-	V
		V_{CC}=3.6V		1.48	-	-	V
		V_{CC}=4.5V to 5.0V		2.03	-	-	V
		V_{CC}=5.5V		2.11	-	-	V
V_{IL}	LOW-level input voltage	V_{CC}=1.65V to 2.0V		-	-	0.55	V
		V_{CC}=2.25V to 2.75V		-	-	0.71	V
		V_{CC}=3.0V to 3.6V		-	-	0.65	V
		V_{CC}=4.5V to 5.5V		-	-	0.80	V
V_{OH}	HIGH-level output voltage	V_I = V_{IH} or V_{IL}	V_{CC}=1.65V to 5.5V; I_O=-20uA	V_{CC}-0.1	-	-	V
			V_{CC}=1.65V; I_O=-2mA	1.21	-	-	V
			V_{CC}=1.8V; I_O=-2mA	1.45	-	-	V
			V_{CC}=2.3V; I_O=-2.3mA	2.0	-	-	V
			V_{CC}=2.3V; I_O=-3mA	1.93	-	-	V
			V_{CC}=2.5V; I_O=-3mA	2.15	-	-	V
			V_{CC}=3.0V; I_O=-3mA	2.7	-	-	V
			V_{CC}=3.0V; I_O=-5.5mA	2.49	-	-	V
			V_{CC}=3.3V; I_O=-5.5mA	2.8	-	-	V
			V_{CC}=4.5V; I_O=-4mA	4.1	-	-	V
			V_{CC}=4.5V; I_O=-8mA	3.95	-	-	V
			V_{CC}=5.0V; I_O=-8mA	4.5	-	-	V

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}	V_{CC}=1.65V to 5.5V; I_O=20uA	-	-	0.1	V
			V_{CC}=1.65V; I_O=2mA	-	-	0.25	V
			V_{CC}=2.3V; I_O=2.3mA	-	-	0.15	V
			V_{CC}=2.3V; I_O=3mA	-	-	0.2	V
			V_{CC}=3.0V; I_O=3mA	-	-	0.15	V
			V_{CC}=3.0V; I_O=5.5mA	-	-	0.252	V
			V_{CC}=4.5V; I_O=4mA	-	-	0.2	V
			V_{CC}=4.5V; I_O=8mA	-	-	0.35	V
I_I	Input leakage current	V_I=V_{CC} or GND; V_{CC}=0V to 5.5V		-	-	±1	uA
I_{OZ}	OFF-state output current	V_{CC}=5V; V_I=V_{IH} or V_{IL}; V_O=V_{CC} or GND		-	-	±2.5	uA
I_{CC}	Supply current	V_I=V_{CC} or GND; I_O=0A; V_{CC}=1.8V, 2.5V, 3.3V, 5.0V		-	-	10	uA
ΔI_{CC}	Additional supply current	Per input pin; V_{CC}=1.8V; V_I=0.3V or 1.1V; I_O=0A; Other pins at V_{CC} or GND		-	-	10	uA
		Per input pin; V_{CC}=5.5V; V_I=0.3V or 3.4V; I_O=0A; Other pins at V_{CC} or GND		-	-	1.5	mA

7.4.3 DC Characteristics 3

T_{amb}=-40°C to +125°C, voltages are referenced to GND (ground=0V), unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{IH}	HIGH-level input voltage	V_{CC}=1.65V to 1.8V	1.0	-	-	V
		V_{CC}=2.0V	1.03	-	-	V
		V_{CC}=2.25V to 2.5V	1.18	-	-	V
		V_{CC}=2.75V	1.23	-	-	V
		V_{CC}=3.0V to 3.3V	1.37	-	-	V
		V_{CC}=3.6V	1.48	-	-	V
		V_{CC}=4.5V to 5.0V	2.03	-	-	V
		V_{CC}=5.5V	2.11	-	-	V
V_{IL}	LOW-level input voltage	V_{CC}=1.65V to 2.0V	-	-	0.55	V
		V_{CC}=2.25V to 2.75V	-	-	0.71	V
		V_{CC}=3.0V to 3.6V	-	-	0.65	V
		V_{CC}=4.5V to 5.5V	-	-	0.80	V

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}	V_{CC}=1.65V to 5.5V; I_O=-20uA	V_{CC}-0.1	-	-	V
			V_{CC}=1.65V; I_O=-2mA	1.21	-	-	V
			V_{CC}=1.8V; I_O=-2mA	1.45	-	-	V
			V_{CC}=2.3V; I_O=-2.3mA	2.0	-	-	V
			V_{CC}=2.3V; I_O=-3mA	1.93	-	-	V
			V_{CC}=2.5V; I_O=-3mA	2.15	-	-	V
			V_{CC}=3.0V; I_O=-3mA	2.7	-	-	V
			V_{CC}=3.0V; I_O=-5.5mA	2.49	-	-	V
			V_{CC}=3.3V; I_O=-5.5mA	2.8	-	-	V
			V_{CC}=4.5V; I_O=-4mA	4.1	-	-	V
			V_{CC}=4.5V; I_O=-8mA	3.95	-	-	V
			V_{CC}=5.0V; I_O=-8mA	4.5	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}	V_{CC}=1.65V to 5.5V; I_O=20uA	-	-	0.1	V
			V_{CC}=1.65V; I_O=2mA	-	-	0.25	V
			V_{CC}=2.3V; I_O=2.3mA	-	-	0.15	V
			V_{CC}=2.3V; I_O=3mA	-	-	0.2	V
			V_{CC}=3.0V; I_O=3mA	-	-	0.15	V
			V_{CC}=3.0V; I_O=5.5mA	-	-	0.252	V
			V_{CC}=4.5V; I_O=4mA	-	-	0.2	V
			V_{CC}=4.5V; I_O=8mA	-	-	0.35	V
I_I	Input leakage current	V_I=V_{CC} or GND; V_{CC}=0V to 5.5V		-	-	±1	uA
I_{OZ}	OFF-state output current	V_{CC}=5V; V_I=V_{IH} or V_{IL}; V_O=V_{CC} or GND		-	-	±2.5	uA
I_{CC}	Supply current	V_I=V_{CC} or GND; I_O=0A; V_{CC}=1.8V, 2.5V, 3.3V, 5.0V		-	-	10	uA
ΔI_{CC}	Additional supply current	Per input pin; V_{CC}=1.8V; V_I=0.3V or 1.1V; I_O=0A; Other pins at V_{CC} or GND		-	-	10	uA
		Per input pin; V_{CC}=5.5V; V_I=0.3V or 3.4V; I_O=0A; Other pins at V_{CC} or GND		-	-	1.5	mA

7.4.4 AC Characteristics 1

T_{amb}=25°C, GND=0V, unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
t_{pd}	Propagation delay	A to Y; See Figure 8-4(1)	V_{CC}=1.8V; C_L=15pF	-	8.5	12.5	ns
			V_{CC}=1.8V; C_L=30pF	-	9.9	14.0	ns
			V_{CC}=2.5V; C_L=15pF	-	6.0	8.6	ns
			V_{CC}=2.5V; C_L=30pF	-	6.9	9.6	ns
			V_{CC}=3.3V; C_L=15pF	-	4.9	7.0	ns
			V_{CC}=3.3V; C_L=30pF	-	5.7	7.8	ns
			V_{CC}=5.0V; C_L=15pF	-	4.2	5.3	ns
			V_{CC}=5.0V; C_L=30pF	-	4.7	6.0	ns
t_{en}	Enable time	OE to Y; See Figure 8-5(1)	V_{CC}=1.8V; C_L=15pF	-	7.4	11.7	ns
			V_{CC}=1.8V; C_L=30pF	-	8.8	14.2	ns
			V_{CC}=2.5V; C_L=15pF	-	5.1	7.5	ns
			V_{CC}=2.5V; C_L=30pF	-	6.1	9.1	ns
			V_{CC}=3.3V; C_L=15pF	-	4.0	5.9	ns
			V_{CC}=3.3V; C_L=30pF	-	4.9	7.0	ns
			V_{CC}=5.0V; C_L=15pF	-	3.8	5.1	ns
			V_{CC}=5.0V; C_L=30pF	-	4.4	5.9	ns
t_{dis}	Disable time	OE to Y; See Figure 8-5(1)	V_{CC}=1.8V; C_L=15pF	-	9.7	12.4	ns
			V_{CC}=1.8V; C_L=30pF	-	12.5	15.3	ns
			V_{CC}=2.5V; C_L=15pF	-	7.0	8.5	ns
			V_{CC}=2.5V; C_L=30pF	-	8.9	10.5	ns
			V_{CC}=3.3V; C_L=15pF	-	5.7	6.9	ns
			V_{CC}=3.3V; C_L=30pF	-	7.2	8.4	ns
			V_{CC}=5.0V; C_L=15pF	-	4.3	5.7	ns
			V_{CC}=5.0V; C_L=30pF	-	5.2	6.2	ns

(1) t_{pd} is the same as t_{PLH} and t_{PHL}.

7.4.5 AC Characteristics 2

T_{amb}=-40°C to +85°C, GND=0V, unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
t_{pd}	Propagation delay	A to Y; See Figure 8-4(1)	V_{CC}=1.8V; C_L=15pF	-	-	14.0	ns
			V_{CC}=1.8V; C_L=30pF	-	-	15.9	ns
			V_{CC}=2.5V; C_L=15pF	-	-	9.8	ns
			V_{CC}=2.5V; C_L=30pF	-	-	10.9	ns
			V_{CC}=3.3V; C_L=15pF	-	-	7.8	ns
			V_{CC}=3.3V; C_L=30pF	-	-	8.8	ns
			V_{CC}=5.0V; C_L=15pF	-	-	5.7	ns
			V_{CC}=5.0V; C_L=30pF	-	-	6.6	ns
t_{en}	Enable time	OE to Y; See Figure 8-5(1)	V_{CC}=1.8V; C_L=15pF	-	-	13.4	ns
			V_{CC}=1.8V; C_L=30pF	-	-	16.3	ns
			V_{CC}=2.5V; C_L=15pF	-	-	8.7	ns
			V_{CC}=2.5V; C_L=30pF	-	-	10.5	ns
			V_{CC}=3.3V; C_L=15pF	-	-	6.8	ns
			V_{CC}=3.3V; C_L=30pF	-	-	7.9	ns
			V_{CC}=5.0V; C_L=15pF	-	-	5.7	ns
			V_{CC}=5.0V; C_L=30pF	-	-	6.6	ns
t_{dis}	Disable time	OE to Y; See Figure 8-5(1)	V_{CC}=1.8V; C_L=15pF	-	-	13.6	ns
			V_{CC}=1.8V; C_L=30pF	-	-	16.6	ns
			V_{CC}=2.5V; C_L=15pF	-	-	9.5	ns
			V_{CC}=2.5V; C_L=30pF	-	-	11.5	ns
			V_{CC}=3.3V; C_L=15pF	-	-	7.5	ns
			V_{CC}=3.3V; C_L=30pF	-	-	9.0	ns
			V_{CC}=5.0V; C_L=15pF	-	-	6.0	ns
			V_{CC}=5.0V; C_L=30pF	-	-	6.5	ns

(1) t_{pd} is the same as t_{PLH} and t_{PHL}.

7.4.6 AC Characteristics 3

T_{amb}=-40°C to +125°C, GND=0V, unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
t_{pd}	Propagation delay	A to Y; See Figure 8-4(1)	V_{CC}=1.8V; C_L=15pF	-	-	15.1	ns
			V_{CC}=1.8V; C_L=30pF	-	-	17.2	ns
			V_{CC}=2.5V; C_L=15pF	-	-	10.4	ns
			V_{CC}=2.5V; C_L=30pF	-	-	11.8	ns
			V_{CC}=3.3V; C_L=15pF	-	-	8.3	ns
			V_{CC}=3.3V; C_L=30pF	-	-	9.5	ns
			V_{CC}=5.0V; C_L=15pF	-	-	6.1	ns
			V_{CC}=5.0V; C_L=30pF	-	-	7.0	ns
t_{en}	Enable time	OE to Y; See Figure 8-5(1)	V_{CC}=1.8V; C_L=15pF	-	-	14.4	ns
			V_{CC}=1.8V; C_L=30pF	-	-	17.6	ns
			V_{CC}=2.5V; C_L=15pF	-	-	9.5	ns
			V_{CC}=2.5V; C_L=30pF	-	-	11.3	ns
			V_{CC}=3.3V; C_L=15pF	-	-	7.3	ns
			V_{CC}=3.3V; C_L=30pF	-	-	8.7	ns
			V_{CC}=5.0V; C_L=15pF	-	-	6.2	ns
			V_{CC}=5.0V; C_L=30pF	-	-	7.2	ns
t_{dis}	Disable time	OE to Y; See Figure 8-5(1)	V_{CC}=1.8V; C_L=15pF	-	-	14.5	ns
			V_{CC}=1.8V; C_L=30pF	-	-	17.5	ns
			V_{CC}=2.5V; C_L=15pF	-	-	10.0	ns
			V_{CC}=2.5V; C_L=30pF	-	-	12.0	ns
			V_{CC}=3.3V; C_L=15pF	-	-	7.9	ns
			V_{CC}=3.3V; C_L=30pF	-	-	9.4	ns
			V_{CC}=5.0V; C_L=15pF	-	-	6.2	ns
			V_{CC}=5.0V; C_L=30pF	-	-	6.8	ns

(1) t_{pd} is the same as t_{PLH} and t_{PHL}.

8 Detailed Description

8.1 Overview

The CJ74LV1T126 is a single, level translating buffer/line driver with 3-state output.

The low threshold inputs support 1.8V input logic at V_{CC}=3.3V and can be used in 1.8V to 3.3V level up translation. In addition, the 5V tolerant input pins enable level down translation (3.3V to 2.5V output at V_{CC}=2.5V). The output level is referenced to the supply voltage and supports 1.8V, 2.5V, 3.3V and 5.0V CMOS levels.

8.2 Functional Block Diagram

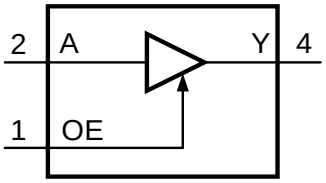


Figure 8-1 Logic symbol

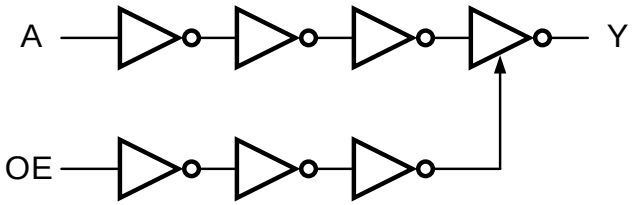


Figure 8-2 Logic diagram

8.3 Function Table

INPUT		OUTPUT
OE	**A**	**Y**
H	L	L
H	H	H
L	X	Z

Note: H=HIGH voltage level; L=LOW voltage level.

8.4 Testing Circuit

8.4.1 AC Testing Circuit

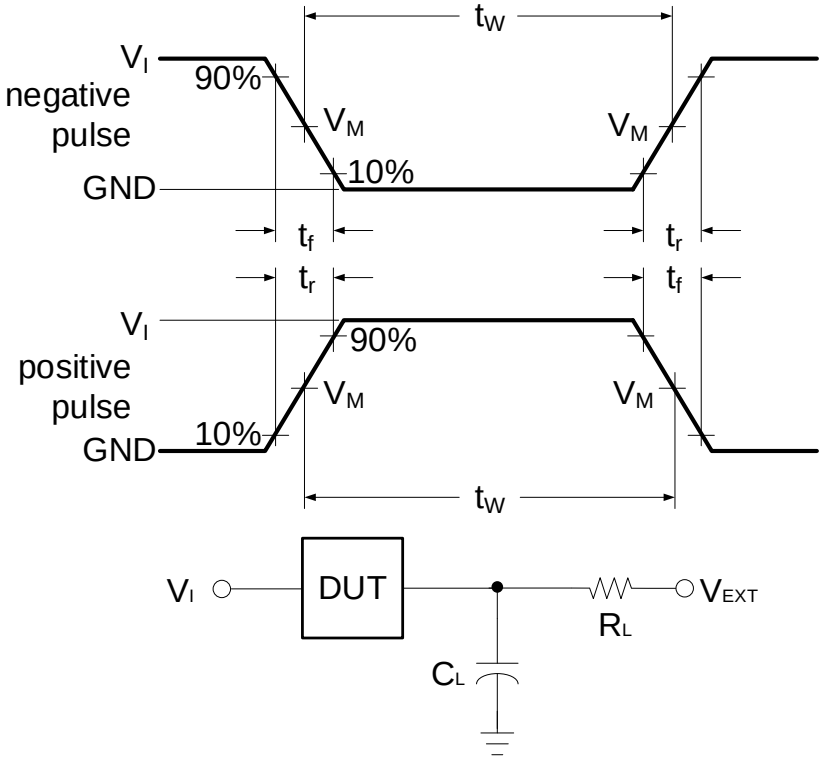


Figure 8-3 Test circuit for measuring switching times

Definitions for test circuit:

C_L=Load capacitance including jig and probe capacitance.

R_L=Load resistance.

8.4.2 AC Testing Waveforms

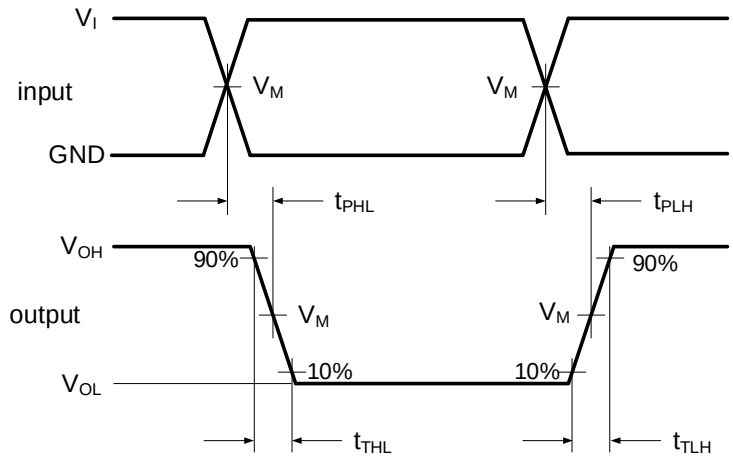


Figure 8-4 The input A to output Y propagation delays

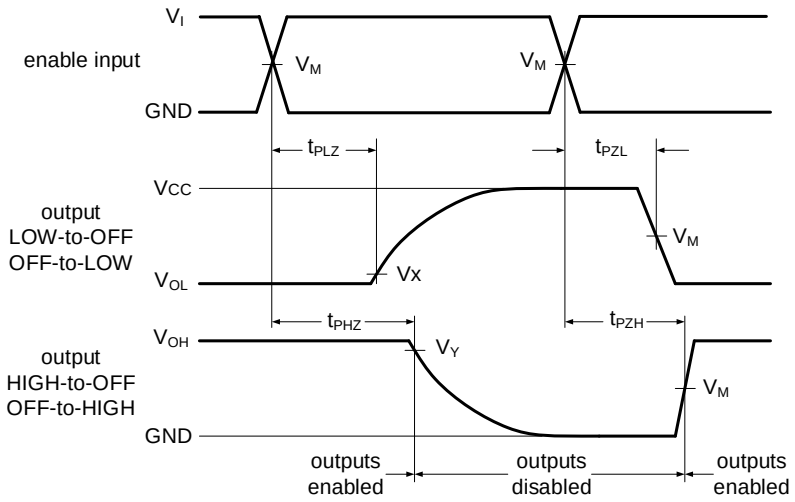


Figure 8-5 3-state enable and disable times

8.4.3 Measurement Points

INPUT	OUTPUT		
V_M	V_M	V_X	V_Y
$0.5V_I$	$0.5V_{CC}$	V_{OL}+0.3V	V_{OH}-0.3V

8.4.4 Test Data

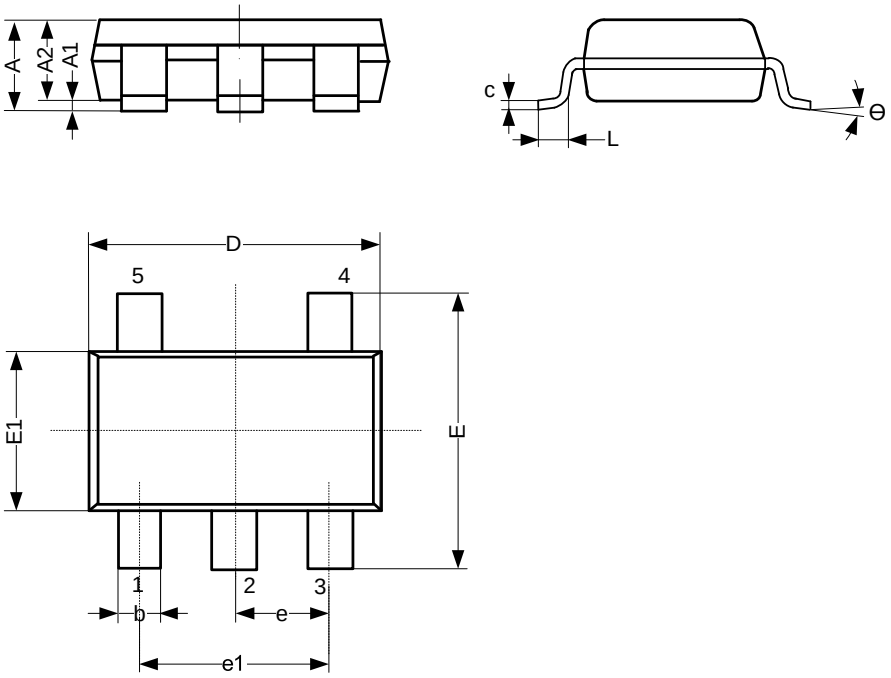
SUPPLY VOLTAGE	INPUT		LOAD		V_{EXT}		
V_{CC}	V_I	$\Delta t/\Delta V$(1)	C_L	R_L	t_{PLH}, t_{PHL}	t_{PZH}, t_{PHZ}	t_{PZL}, t_{PLZ}
1.8V	V_{CC}	≤1.0ns/V	15pF, 30pF	1kΩ	Open	GND	V_{CC}
2.5V	V_{CC}	≤1.0ns/V	15pF, 30pF	1kΩ	Open	GND	V_{CC}
3.3V	3V	≤1.0ns/V	15pF, 30pF	1kΩ	Open	GND	V_{CC}
5.0V	3V	≤1.0ns/V	15pF, 30pF	1kΩ	Open	GND	V_{CC}

(1) dV/dt≥1.0V/ns

9 Mechanical Information

9.1 SOT-23-5L Mechanical Information

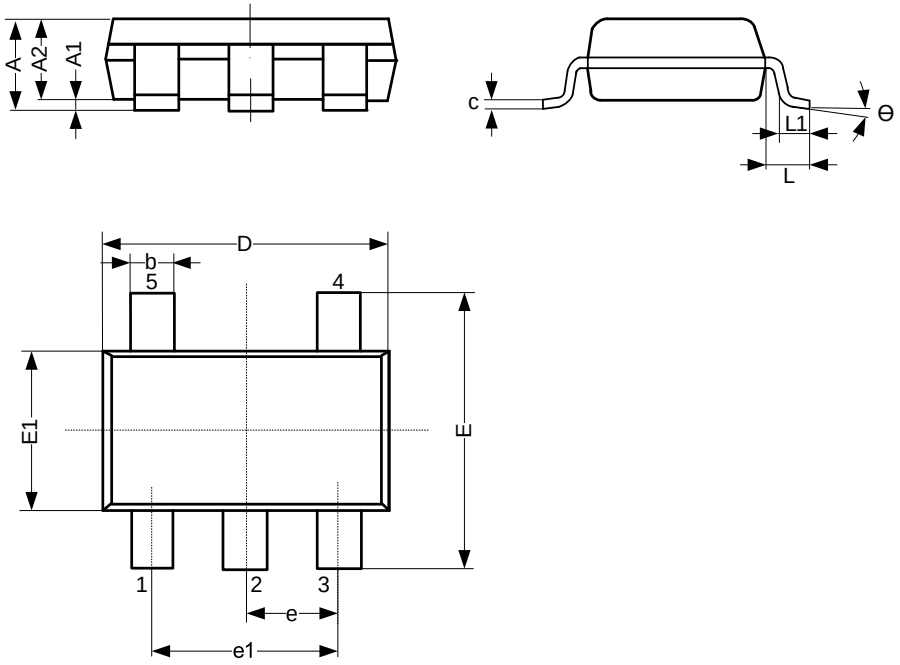
9.1.1 SOT-23-5L Outline Dimensions



SYMBOL	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	-	-	1.26
A1	0.00	-	0.12
A2	1.00	-	1.20
b	0.30	-	0.50
c	0.10	-	0.20
D	2.82	-	3.02
E	2.60	-	3.00
E1	1.50	-	1.70
e	0.95 BSC		
e1	1.80	-	2.00
L	0.30	-	0.60
θ	0°	-	8°
Unit: mm			

9.2 SOT-353 Mechanical Information

9.2.1 SOT-353 Outline Dimensions



SYMBOL	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.90	-	1.10
A1	0.00	-	0.10
A2	0.90	-	1.00
b	0.15	-	0.35
c	0.11	-	0.175
D	2.00	-	2.20
E	2.15	-	2.45
E1	1.15	-	1.35
e	0.65 BSC		
e1	1.20	-	1.40
L	-	0.525	-
L1	0.26	-	0.46
Θ	0°	-	8°
Unit: mm			

10 Notes and Revision History

10.1 Associated Product Family and Others

To view other products of the same type or IC products of other types, click the official website of JSCJ -- ***https:www.jscj-elec.com*** for more details.

10.2 Notes

Electrostatic Discharge Caution

This IC may be damaged by ESD. Relevant personnel shall comply with correct installation and use specifications to avoid ESD damage to the IC. If appropriate measures are not taken to prevent ESD damage, the hazards caused by ESD include but are not limited to degradation of integrated circuit performance or complete damage of integrated circuit. For some precision integrated circuits, a very small parameter change may cause the whole device to be inconsistent with its published specifications.

DISCLAIMER

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